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| Search Notes  | Application/Control No. 10585696 | Applicant(s)/Patent Under Reexamination PETROLI, JEAN PAUL |
| | Examiner SAIF A ALHIJA | Art Unit 2128 |

| SEARCHED | | | |
|----------|----------------------------|---------|----------|
| Class | Subclass | Date | Examiner |
| 703 | 13, 15, 16, 19, 23, and 28 | 9/22/10 | SA |
| 714 | 33 | 9/22/10 | SA |
| 716 | 5, 6, 16 | 9/22/10 | SA |

| SEARCH NOTES | | |
|-------------------------|---------|----------|
| Search Notes | Date | Examiner |
| Google. FPGA scheduling | 9/22/10 | SA |
| EAST. Same as above | 9/22/10 | SA |
| Inventor Name Search | 9/22/10 | SA |
| IEEE. Same as above | 9/22/10 | SA |
| ACM. Same as above | 9/22/10 | SA |

| INTERFERENCE SEARCH | | | |
|---------------------|----------|------|----------|
| Class | Subclass | Date | Examiner |
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